

# POWER FACTOR CORRECTORS

POWER MAGNETIC

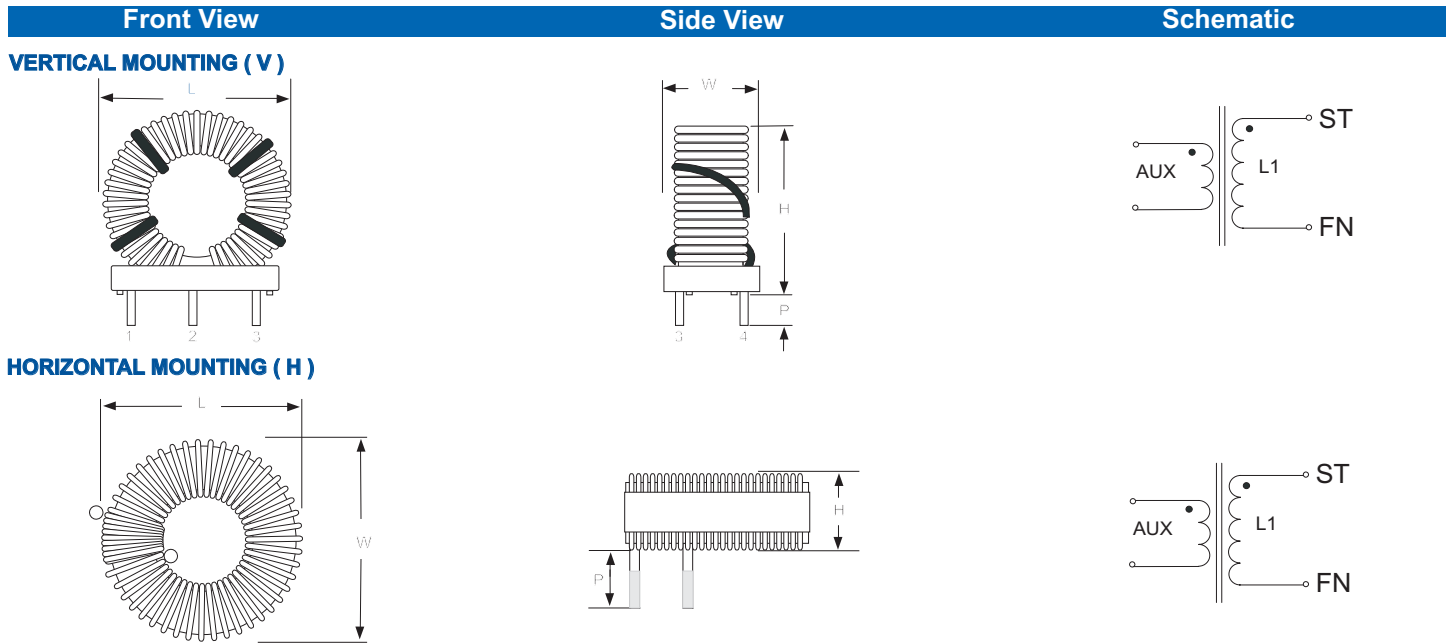
**FALCO**  
electronics

## [ T18-60X1 Series ]



- Designed for use in off-line AC/DC pre-regulators circuits for switching power converters applications.
- Low cores loss and high flux density.
- Auxiliary winding optional.

### MECHANICAL SPECIFICATIONS



### ELECTRICAL SPECIFICATIONS

FPN	Mounting /PCB LAYOUT	L (mm) max.	W (mm) max	H (mm) max	Auxiliary Winding / L1 Ratio	DCR(mΩ) <sup>1</sup> max.	L1 (μH) <sup>1,2</sup>	L1 ST - FN	AUX ST - FN	I @ 80%L (A DC)	I @ 65%L (A DC)	I @ 50%L (A DC)	I @ 30%L (A DC)
T18046	V / FIG.1	54.61	26.67	52.07	---	12	97	1 - 2	---	12.21	19.84	27.47	45.79
T18031	V / FIG.2	57.15	27.18	56.39	---	45	303	2 - 5	---	6.84	11.11	15.38	25.64
T18012	V / FIG.10	60.00	34.00	59.00	---	---	303	1,2,3 - 6,5,4	---	6.84	11.11	15.38	25.64
T18002	H / FIG.3	58.42	58.42	32.26	---	25	314	1 - 2	---	6.33	10.29	14.24	23.74
T18032	V / FIG.4	57.15	31.75	54.86	54:4:3	25	314	2 - 5	1,3 - 6,4	6.33	10.29	14.24	23.74
T18001	H / FIG.5	53.34	53.34	29.97	---	45	540	1 - 2	---	5.10	8.29	11.48	19.13
T18057	V / FIG.6	57.15	32.51	63.50	---	44	594	2 - 5	---	4.88	7.94	10.99	18.31
T18010	V / FIG.7	55.88	28.70	55.88	73:4	95	645	1 - 3	4 - 2	4.68	7.61	10.54	17.56
T18025	V / FIG.7	55.88	28.70	55.88	73:5	95	645	1 - 3	4 - 2	4.68	7.61	10.54	17.56
T180A9	V / FIG.7	55.88	28.70	55.88	---	95	675	1 - 3	---	4.68	7.61	10.54	17.56
T18A25	H / FIG.7	63.50	63.50	31.75	73:5	95	645	4 - 1	3 - 2	4.68	7.61	10.54	17.56
T18056	H / FIG.8	55.88	55.88	31.75	---	63	702	1 - 2	---	4.50	7.31	10.12	16.87
T18060	V / FIG.6	57.15	32.51	63.50	---	85	981	2 - 5	---	3.80	6.17	8.55	14.24
T18053	V / FIG.6	57.15	32.51	63.50	---	135	1098	2 - 5	---	3.598	5.85	8.10	13.49
T18058	V / FIG.6	57.15	32.51	63.50	---	98	1215	2 - 5	---	3.42	5.56	7.69	12.82
T18009	V / FIG.9	55.88	28.70	55.88	104:3	192	1315	1 - 2	3 - 4	3.29	5.34	7.40	12.33
T18055	V / FIG.6	57.15	32.51	63.50	---	292	1746	2 - 5	---	2.85	4.63	6.41	10.68
T18059	V / FIG.6	57.15	32.51	63.50	---	176	2052	2 - 5	---	2.63	4.27	5.92	9.86

\*Contact FALCO for detailed specifications for P dimension

1. AMBIENT TEMPERATURE TEST AT 25°C
2. TEST AT 20KHz and 0.25V. MINIMUM INDUCTANCE.

## PCB LAYOUT SPECIFICATIONS

### SUGGESTED PCB Layout (Top)

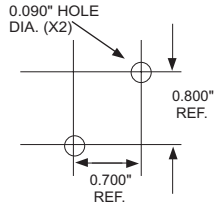


Figure 1

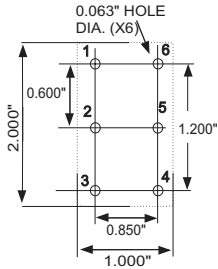


Figure 2

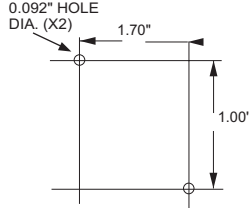


Figure 3

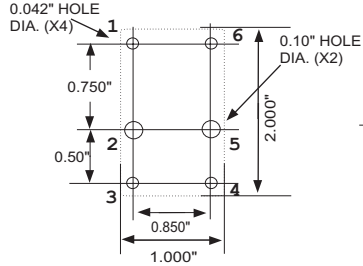


Figure 4

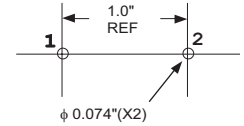
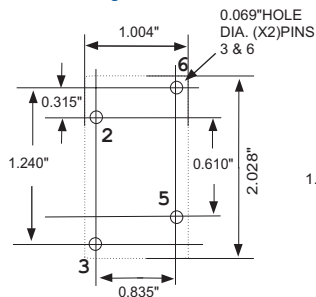


Figure 5



HOLE DIA. (X2) PINS 2 & 5:  
 FOR T18057 0.077"  
 FOR T18060 & T18058 0.065"  
 FOR T18053 0.055"  
 FOR T18055 0.043"  
 FOR T18059 0.057"

Figure 6

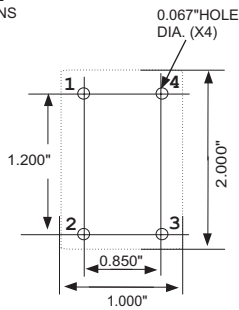


Figure 7

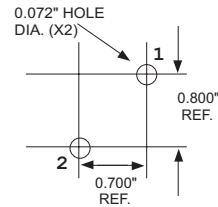


Figure 8

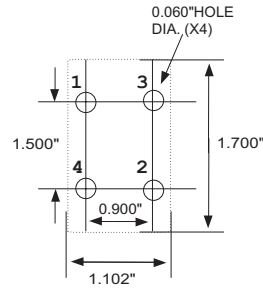


Figure 9

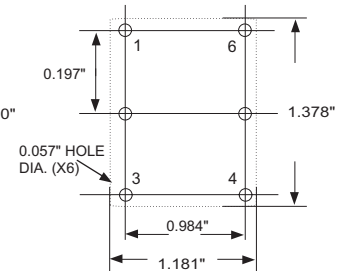


Figure 10